



Parallel to Mipi CSI-2 Bridge

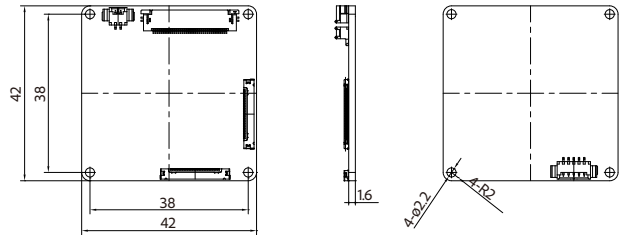
ACE-HMST642-PM

- Parallel to Mipi CSI-2 Bridge(4 Lane@up to 2GHz)
- 2160p@30fps, 1080p@30/60fps, 720p@30/60fps
- 2x Mipi CSI-2 (4 Lanes)
- Jetson Nano, Xavier NX Development Platform
- Physical Board Size : 42 x 42 mm

ACE-HMST642-PM



• Dimensions



Pin assignment

• J201

J201(FFC Connector) / Hirose : FH12-36S-05SH(55) (Input)	
1	RXD : CMOS 5.0V Read Data (Compatible 3.3V)
2	TXD : CMOS 5.0V Send Data (Compatible 3.3V)
3	GND
4	POWER IN: +12VDC
5	POWER IN: +12VDC
6	POWER IN: +12VDC
7	GND
8	GND
9	GND
10	GND
11	GND
12	CAM_VCLK_IN
13	GND
14	HSYNC
15	VSYNC
16	GND
17	C_OUT[7]
18	C_OUT[6]
19	C_OUT[5]
20	C_OUT[4]
21	GND
22	C_OUT[3]
23	C_OUT[2]
24	C_OUT[1]
25	C_OUT[0]
26	GND
27	Y_OUT[7]
28	Y_OUT[6]
29	Y_OUT[5]
30	Y_OUT[4]
31	GND
32	Y_OUT[3]
33	Y_OUT[2]
34	Y_OUT[1]
35	Y_OUT[0]
36	GND

• J204

J204(Micro Co-Axial) / KEL Co.USL00-30L-A (Output 1)-Default	
1	POWER IN: +12VDC
2	POWER IN: +12VDC
3	GND
4	GND
5	INTIO(GPIO)
6	GND
7	MIPI_D0_P(Port-1)
8	MIPI_D0_N(Port-1)
9	GND
10	MIPI_D1_P(Port-1)
11	MIPI_D1_N(Port-1)
12	GND
13	MIPI_CLK_P(Port-1)
14	MIPI_CLK_N(Port-1)
15	GND
16	MIPI_D2_P(Port-1)
17	MIPI_D2_N(Port-1)
18	GND
19	MIPI_D3_P(Port-1)
20	MIPI_D3_N(Port-1)
21	GND
22	I2C_SCL(3.3V)
23	I2C_SDA(3.3V)
24	GND
25	Uart_TX(3.3V)
26	Uart_RX(3.3V)
27	Reset(GPIO)
28	GND
29	NC(GPIO)
30	NC(GPIO)

• J206

J206(Micro Co-Axial) / KEL Co.USL00-30L-A (Output 2)	
1	POWER IN: +12VDC
2	POWER IN: +12VDC
3	GND
4	GND
5	NC(GPIO)
6	GND
7	MIPI_D0_P(Port-2)
8	MIPI_D0_N(Port-2)
9	GND
10	MIPI_D1_P(Port-2)
11	MIPI_D1_N(Port-2)
12	GND
13	MIPI_CLK_P(Port-2)
14	MIPI_CLK_N(Port-2)
15	GND
16	MIPI_D2_P(Port-2)
17	MIPI_D2_N(Port-2)
18	GND
19	MIPI_D3_P(Port-2)
20	MIPI_D3_N(Port-2)
21	GND
22	I2C_SCL(3.3V)
23	I2C_SDA(3.3V)
24	GND
25	Uart_TX(3.3V)
26	Uart_RX(3.3V)
27	Reset(GPIO)
28	GND
29	NC(GPIO)
30	NC(GPIO)

• J301

J301(External Power) / Molex : 53261-0271	
1	Power IN: +12VDC
2	GND